

**Notice of References Cited**Application/Control No.  
09/699,077Applicant(s)/Patent Under  
Reexamination  
ISHIDA ET AL.Examiner  
Ayal I SharonArt Unit  
2123

Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Acuna, E.L. et al. "Simulation Techniques for Mixed Analog / Digital Circuits." IEEE Journal of Solid-State Circuits. Vol.25, Issue 2. pp.353-363.
	V	Carmichael, N. et al. "Simulation as an Aid to Power Supply Diagnostics". AUTOTESTCON '95. Aug. 8-10, 1995. pp.556-560.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.